



# Automatic Parallelization for a Class of Regular Computations

*G M Megson, Xian Chen*

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The automatic generation of parallel code from high level sequential description is of key importance to the wide spread use of high performance machine architectures. This text considers (in detail) the theory and practical realization of automatic mapping of algorithms generated from systems of uniform recurrence equations (do-iccps) onto fixed size architectures with defined communication primitives. Experimental results of the mapping scheme and its implementation are given.

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